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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL. NUM 10021026	FILING DATE 10/30/2001	CLASS 365	SUBCLASS 2818	CAU	EXAMINER
APPLICANTS: Enaboe Michael; Hamlin Christopher					
CONTINUING DATA VERIFIED:					
BEST AVAILABLE COPY					
PREVIOUS APPLICATIONS VERIFIED:					
PUBLISHED DO NOT PUBLISH <input checked="" type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no					
US USCIT 12 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no					
Verified and Acknowledged Examiner's initials					
TITLE: System and method for optimizing an integrated circuit design					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
ISSUE FEE			Total Claims	Print Claim for O.G.
Amount Due	Date Paid	Primary Examiner	DRAWING	
			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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